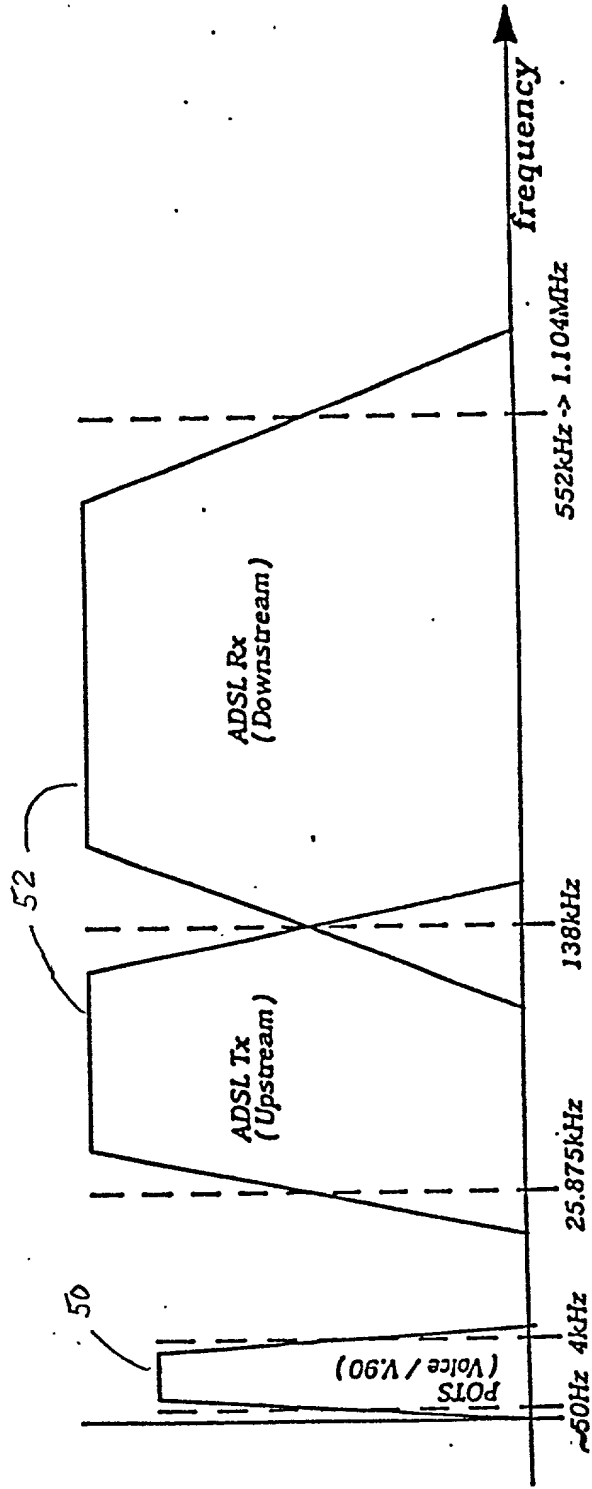


Prior Art

Fig 1



Prior Art

Fig 2

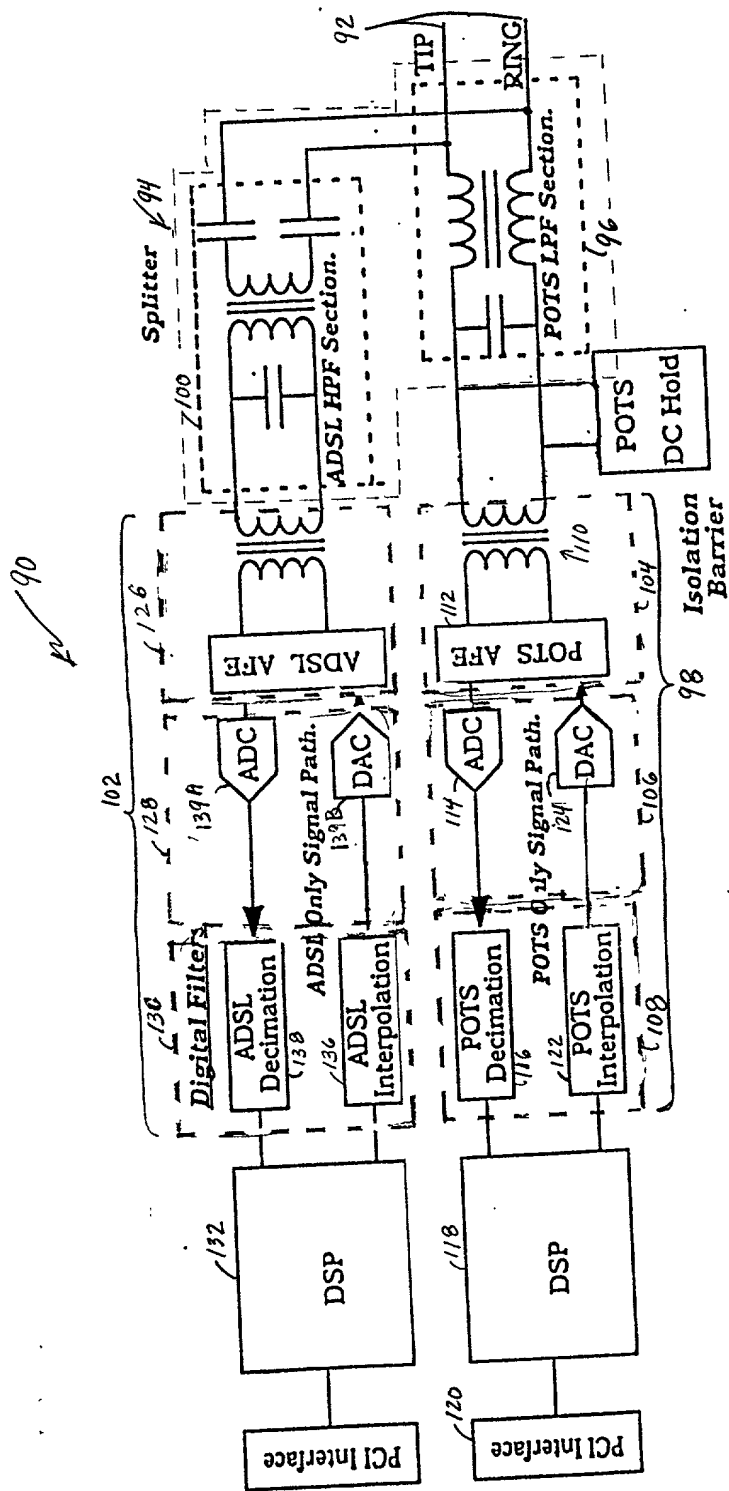


Fig. 3 Prior Art

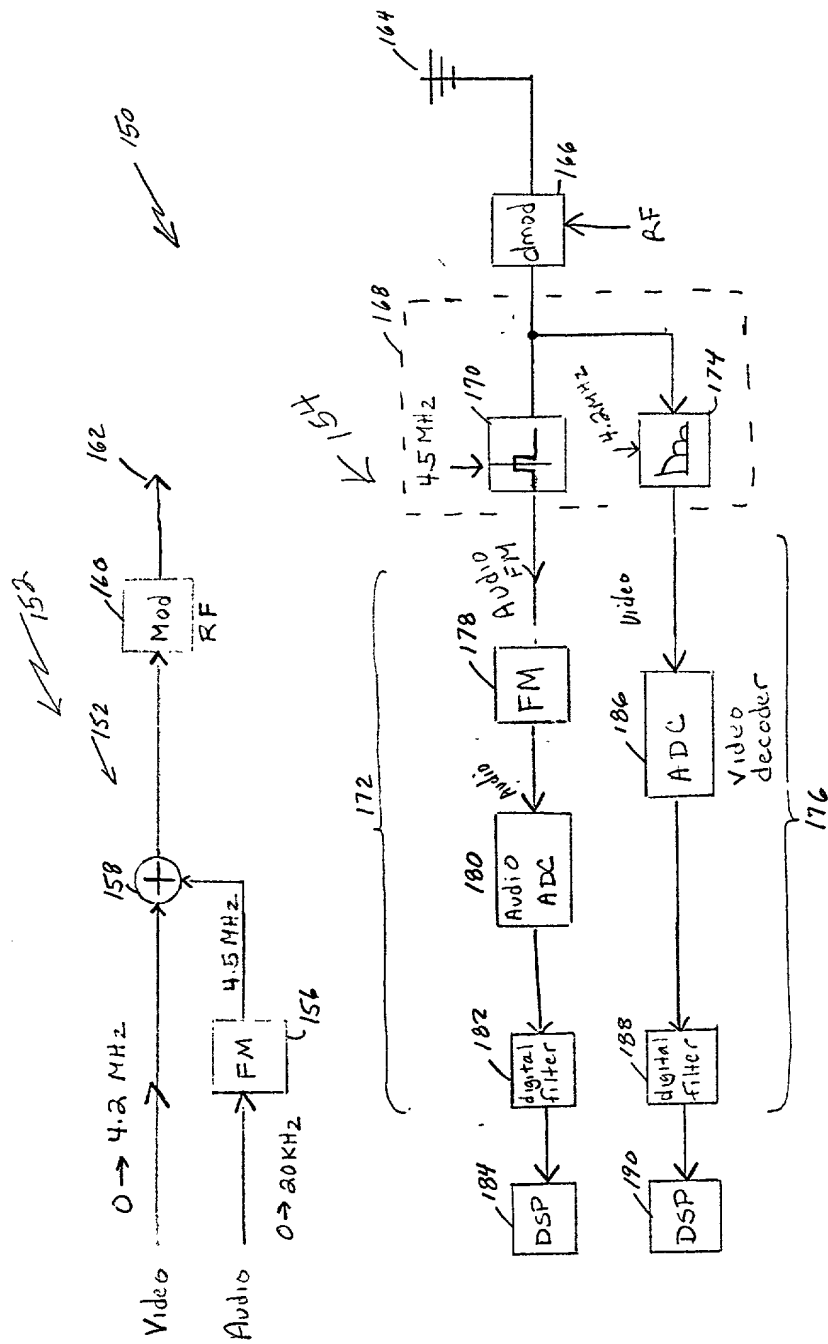


Fig 4 Video filter

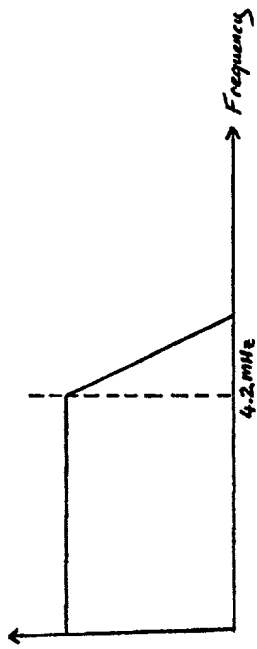


Fig. 5 Prior Art

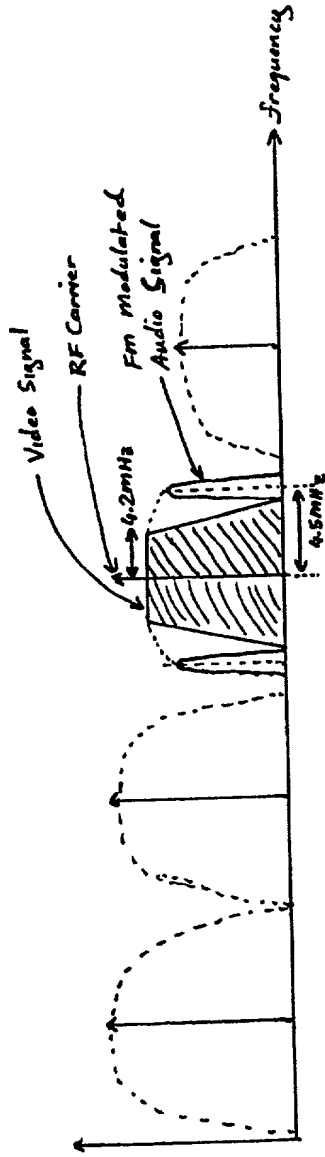


Fig 6 Prior Art

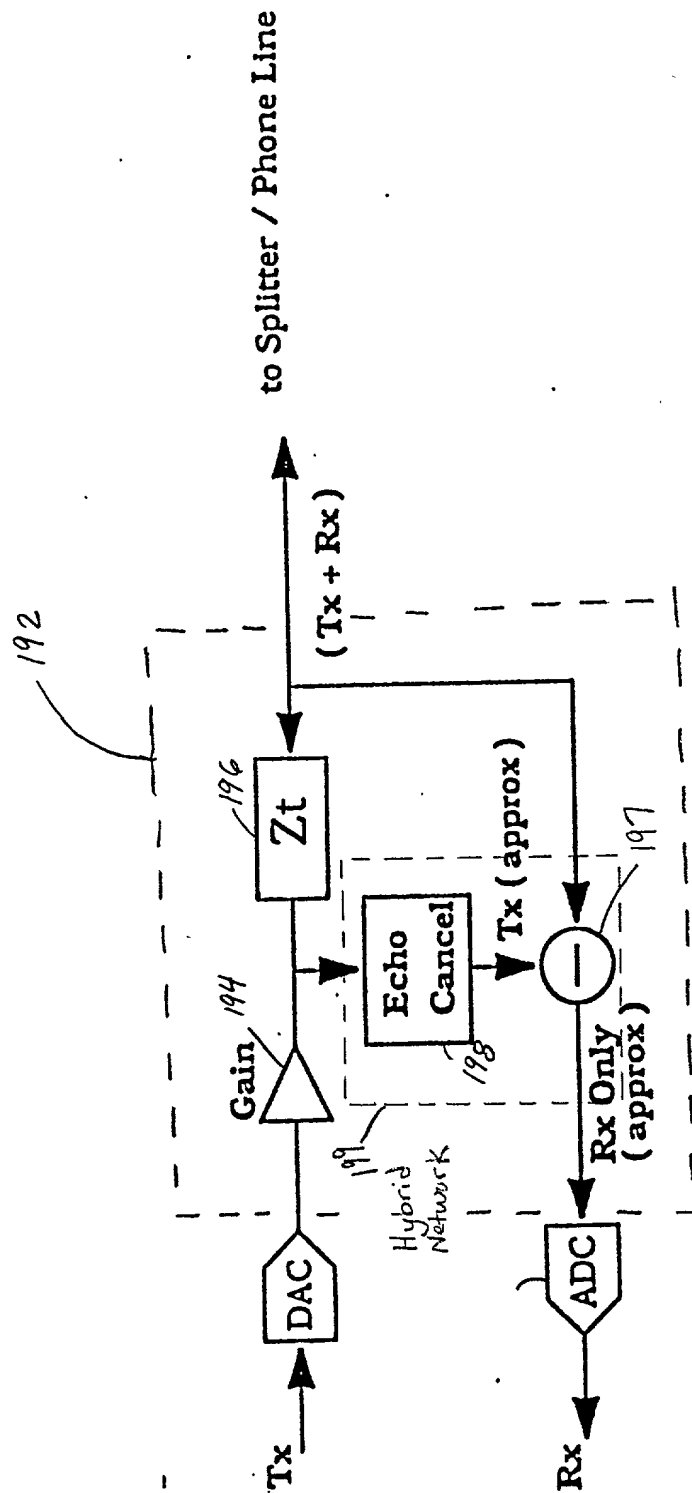


Fig. 7 Prior Art

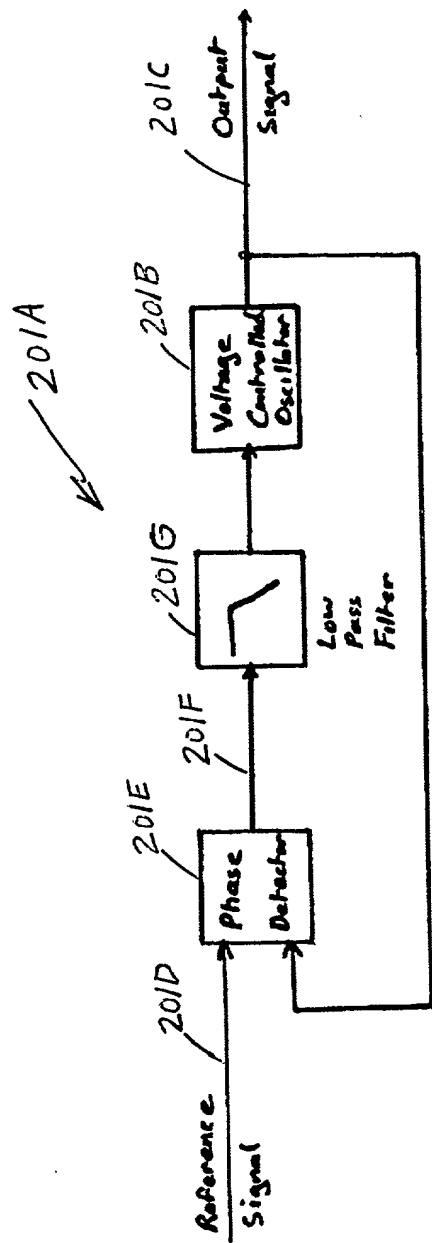


Fig 8 Prior Art

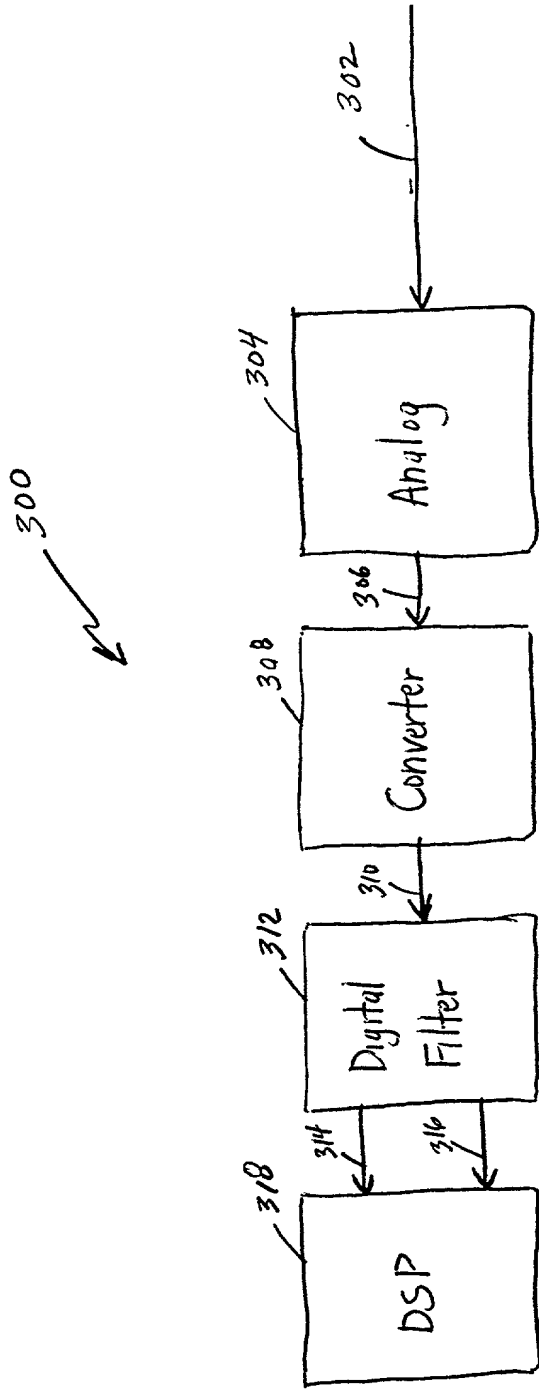
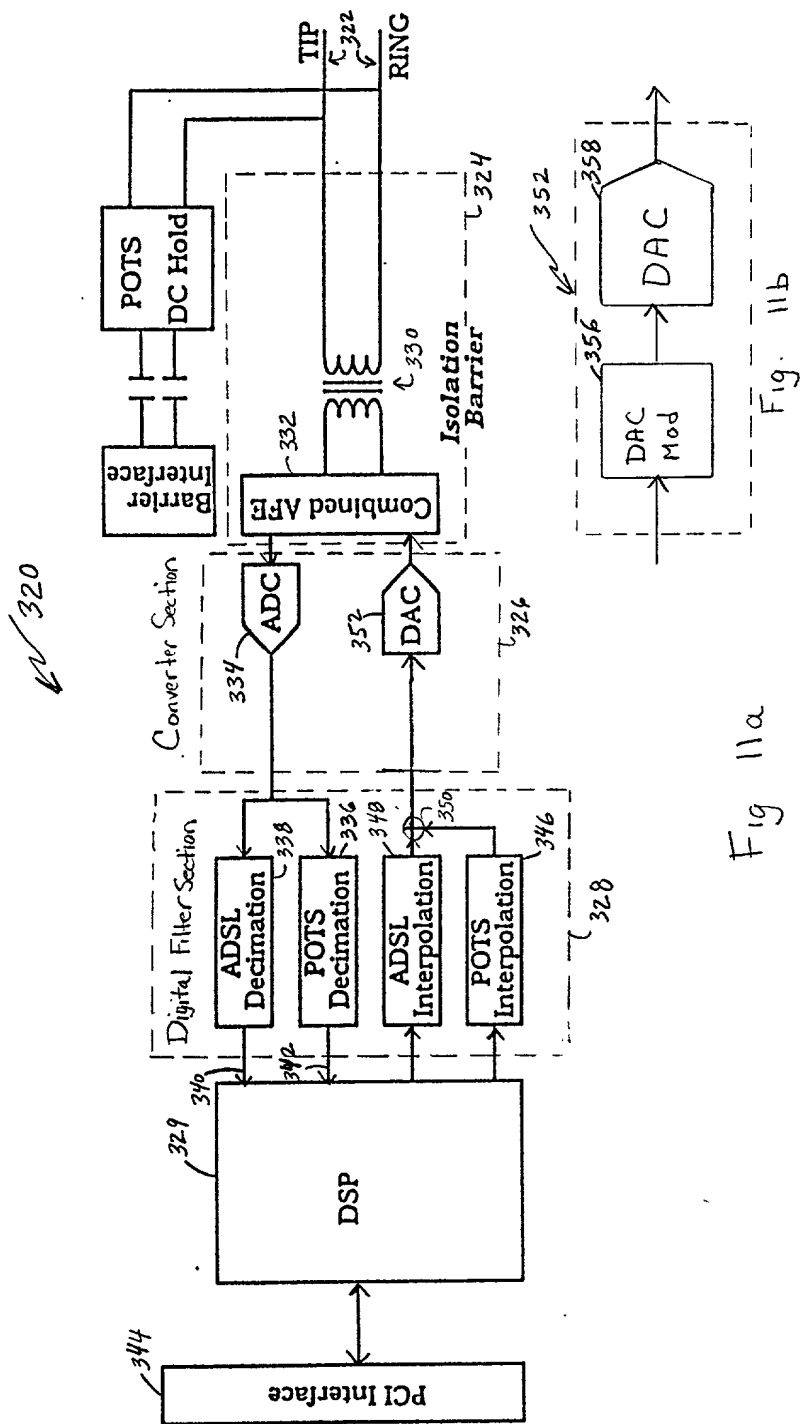


Fig. 10



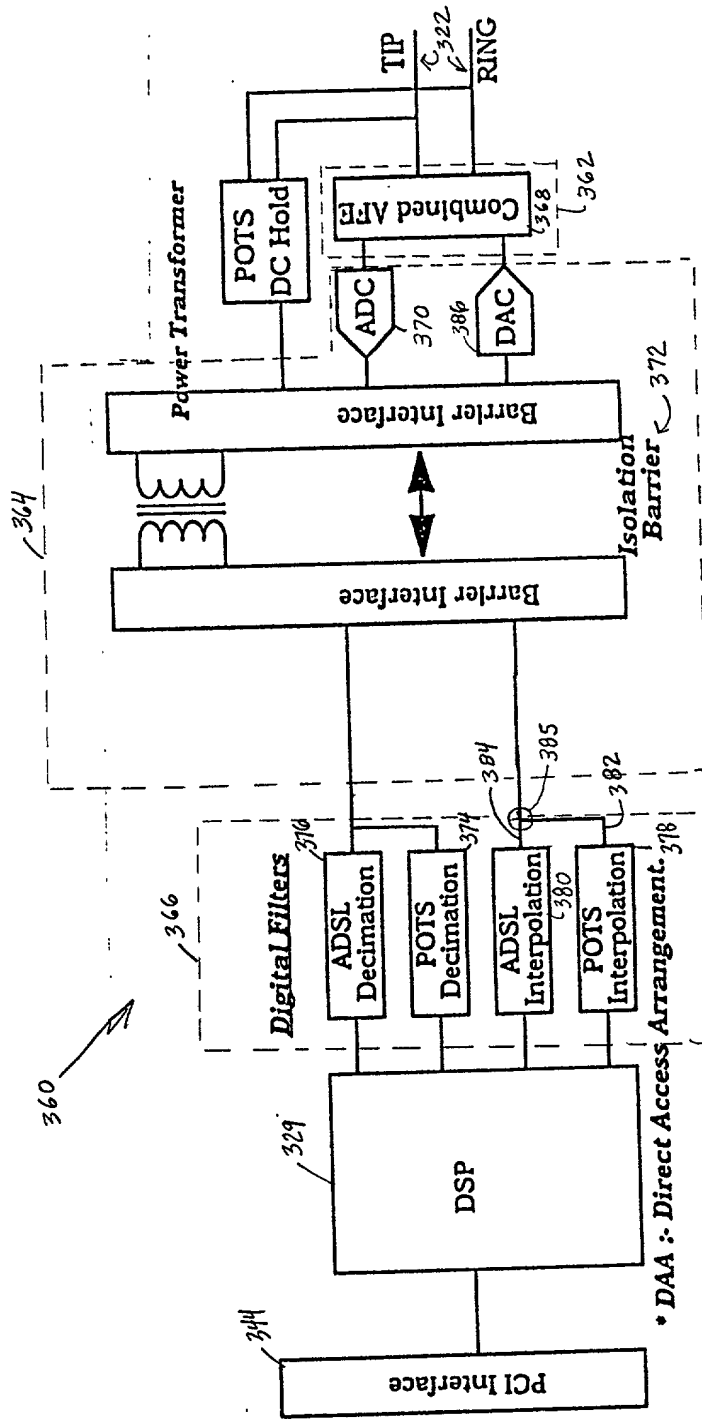
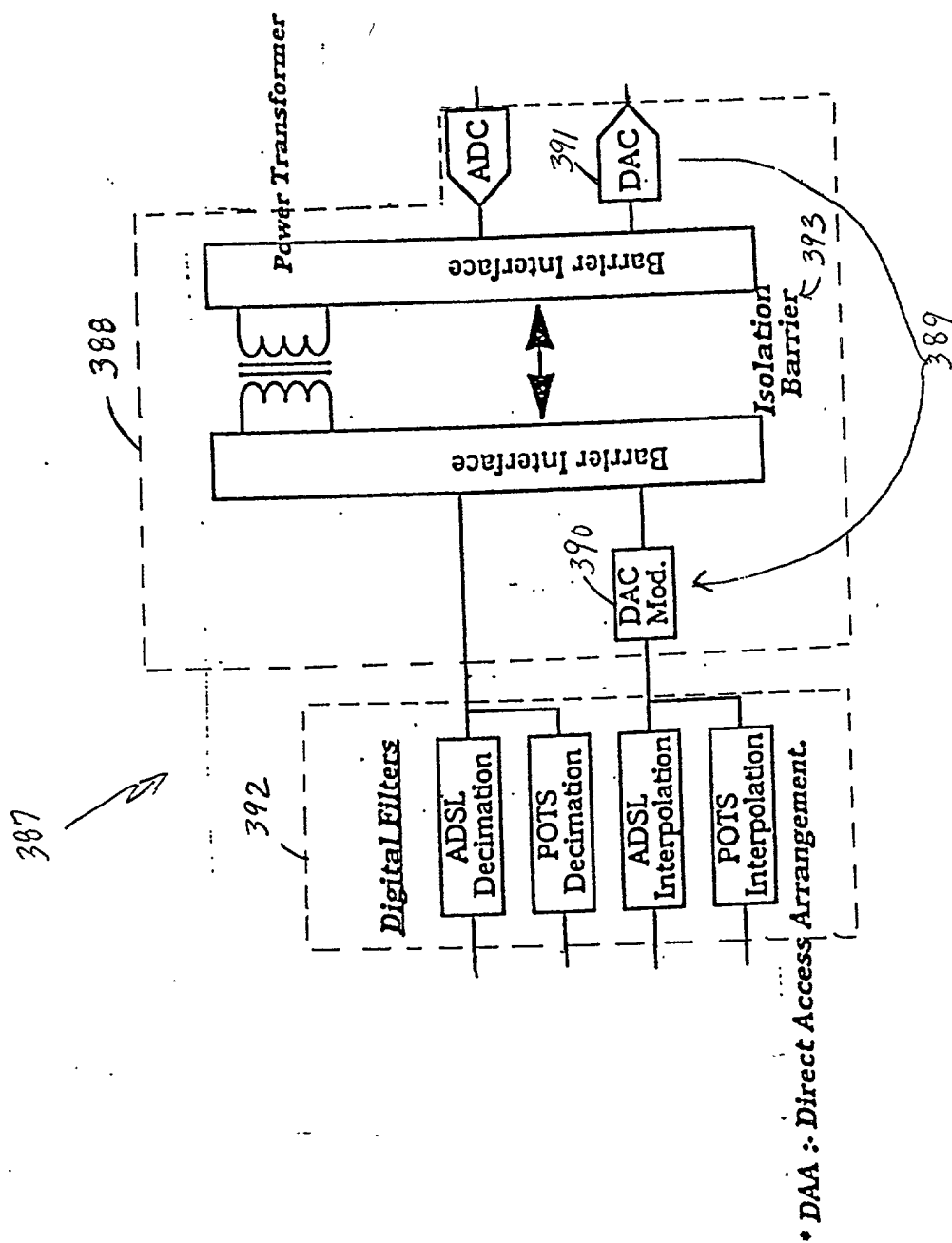


Fig. 12a



F₁₉. 126

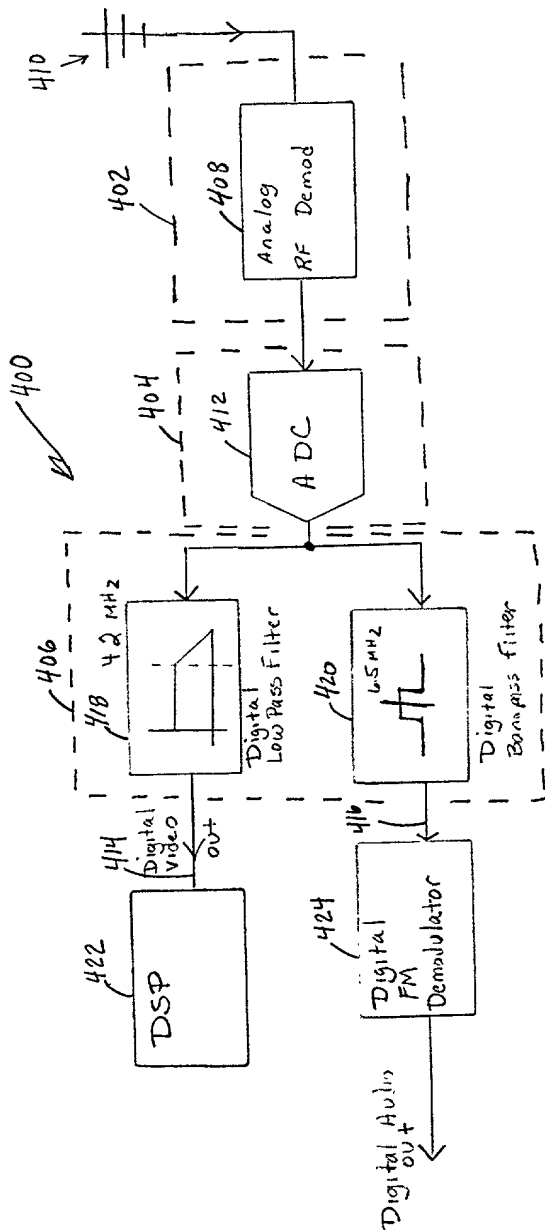


Fig. 13

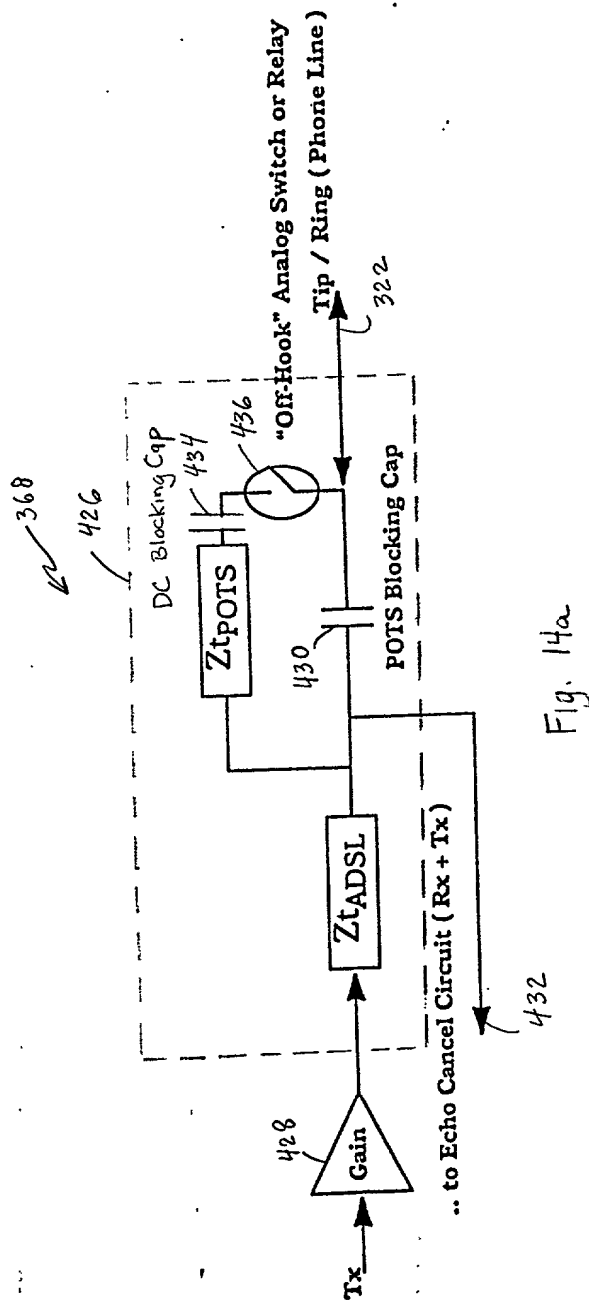


Fig. 14a

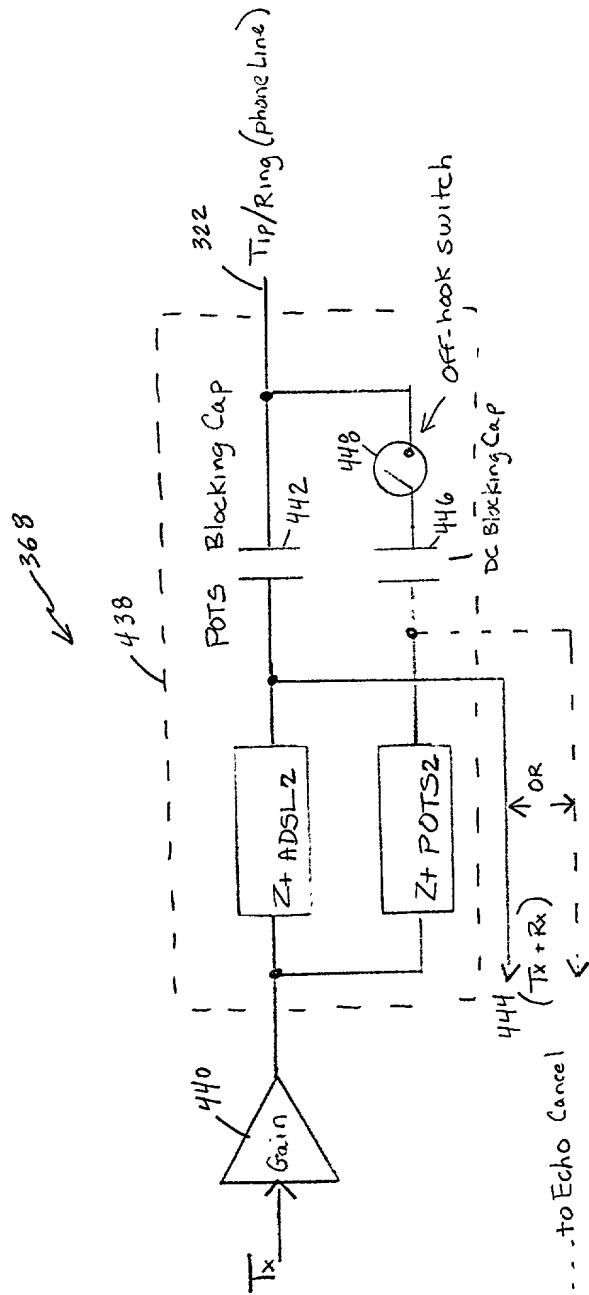


Fig 14b

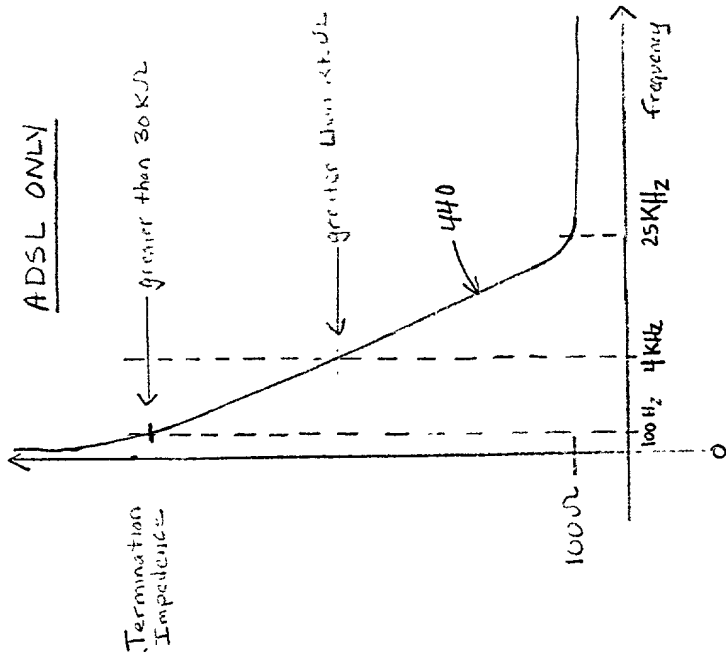


Fig 15a

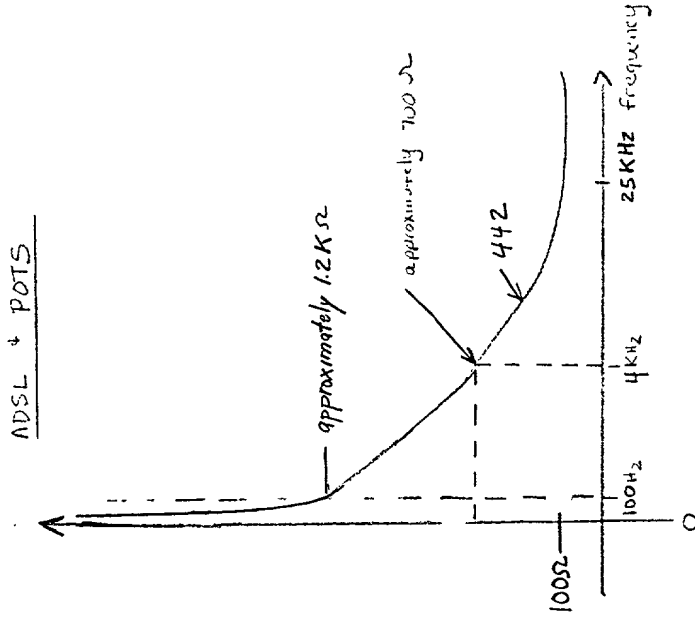


Fig. 15b

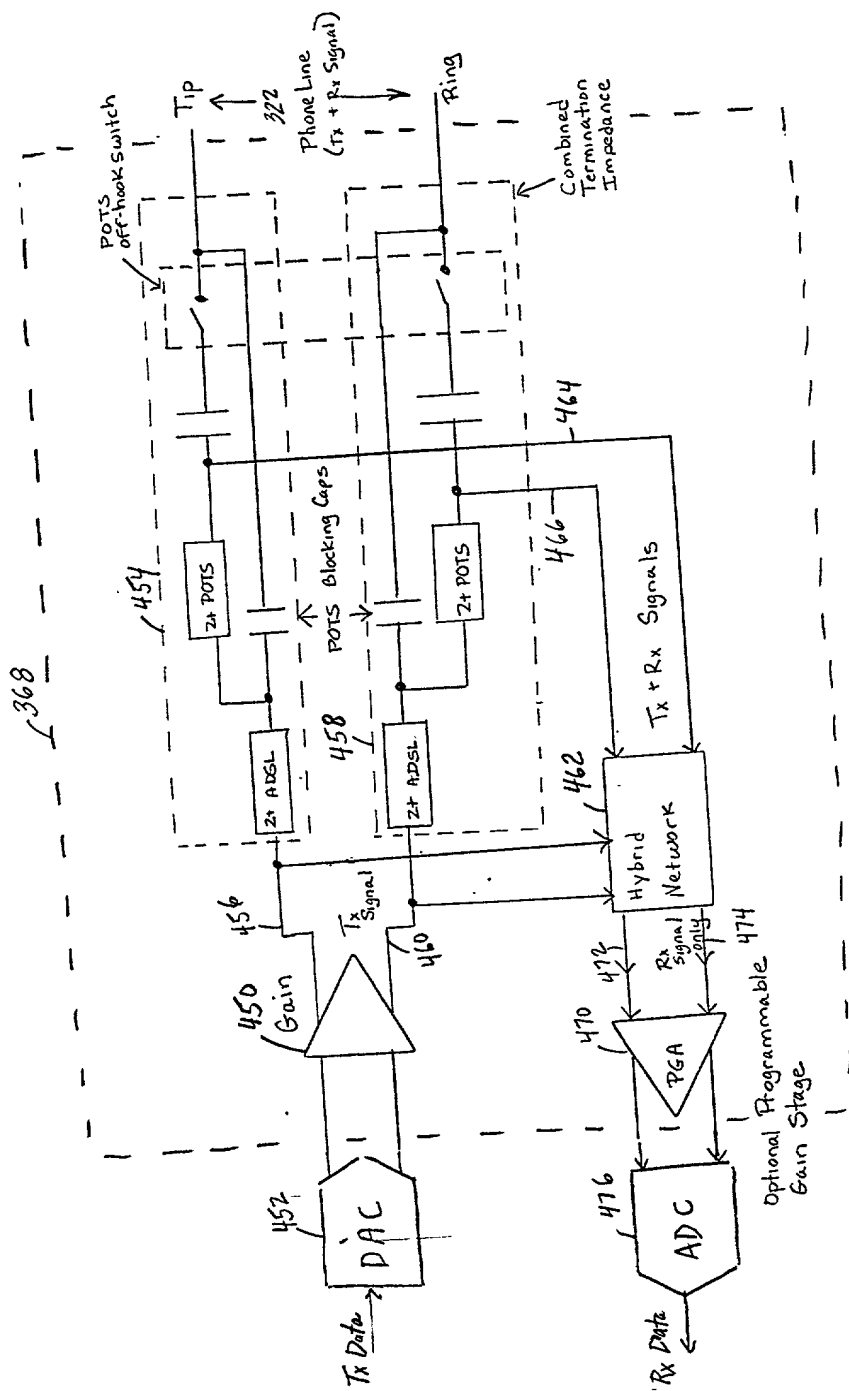
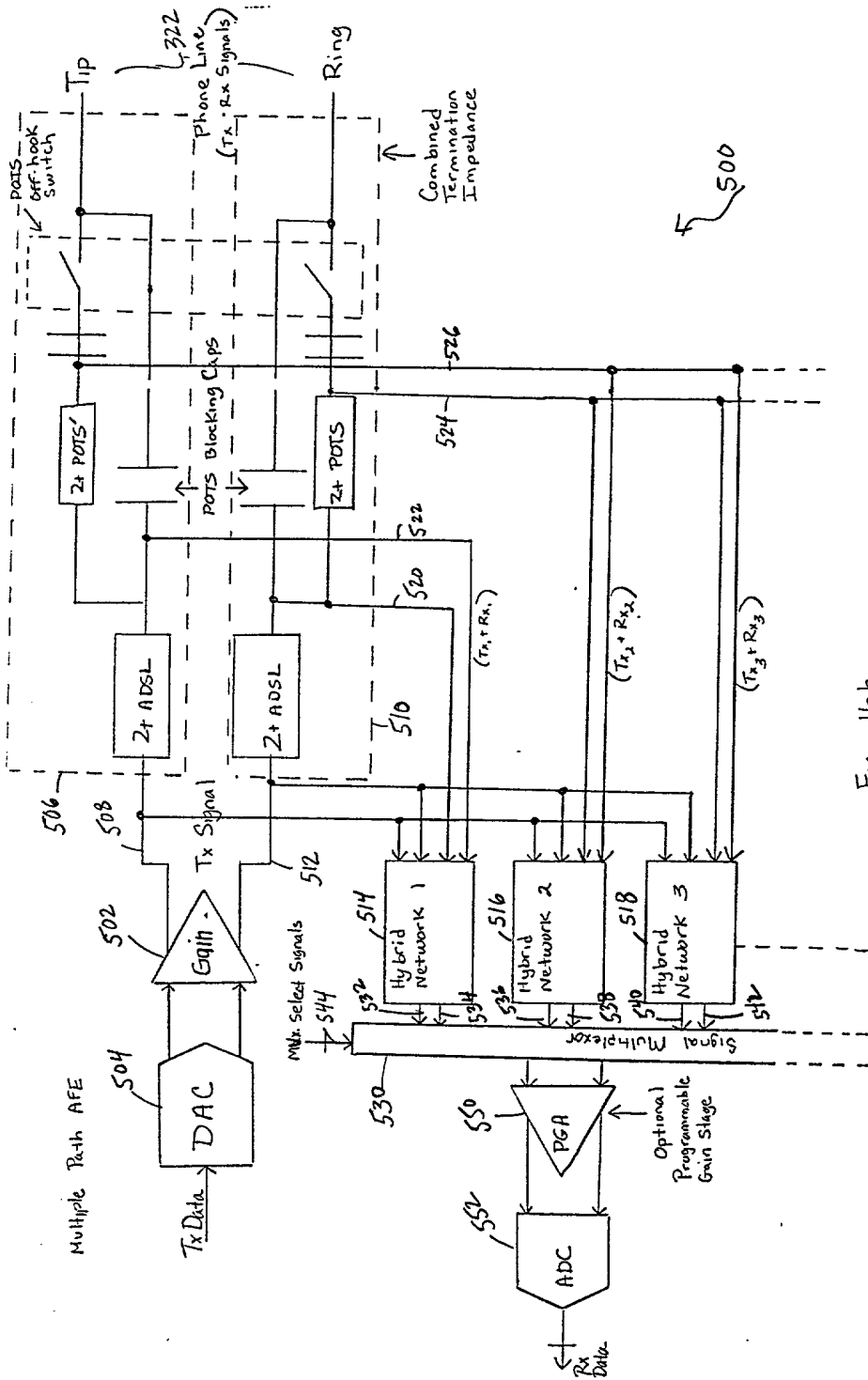


Fig 16a



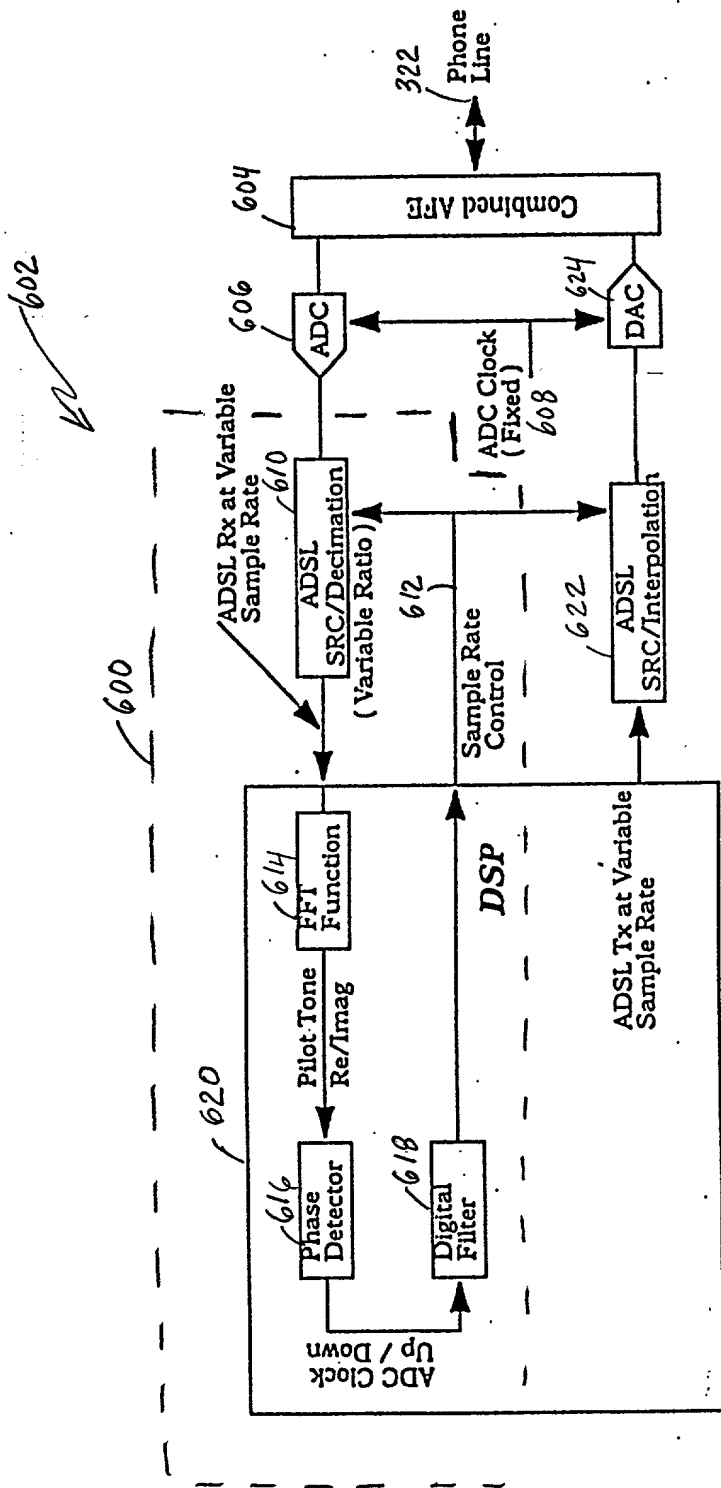


Fig. 17

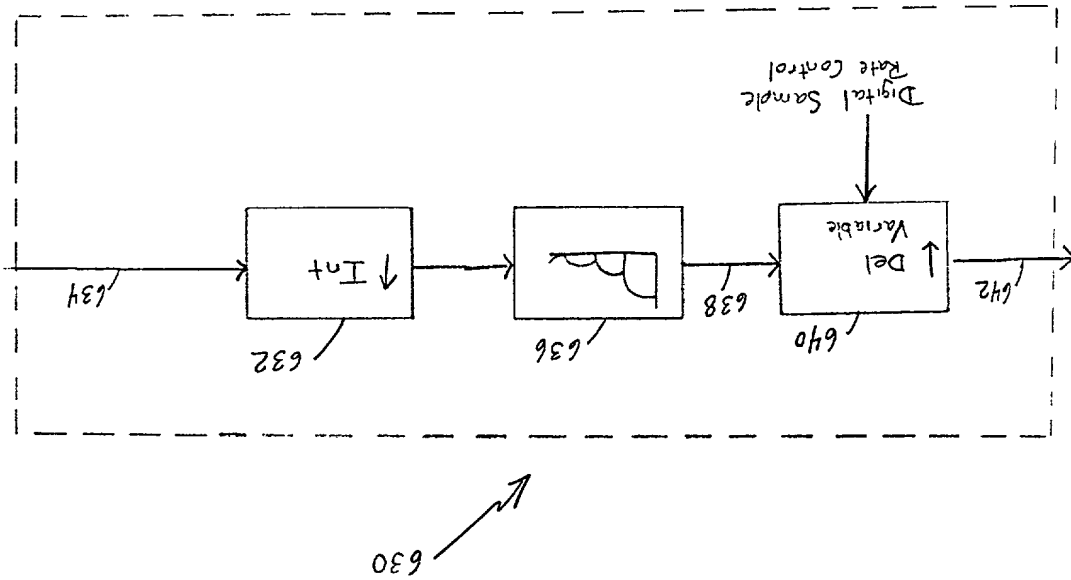
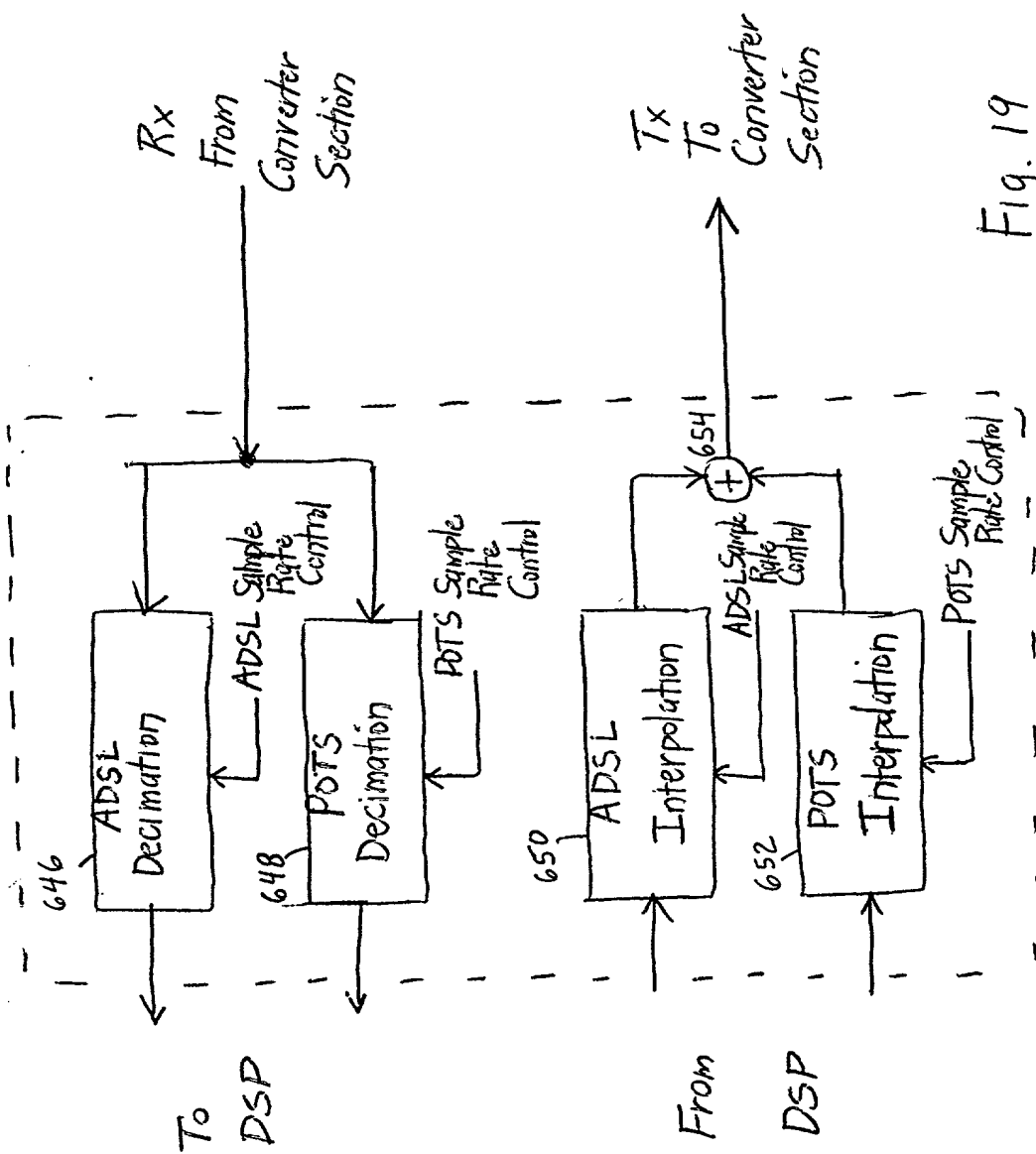


Fig 18



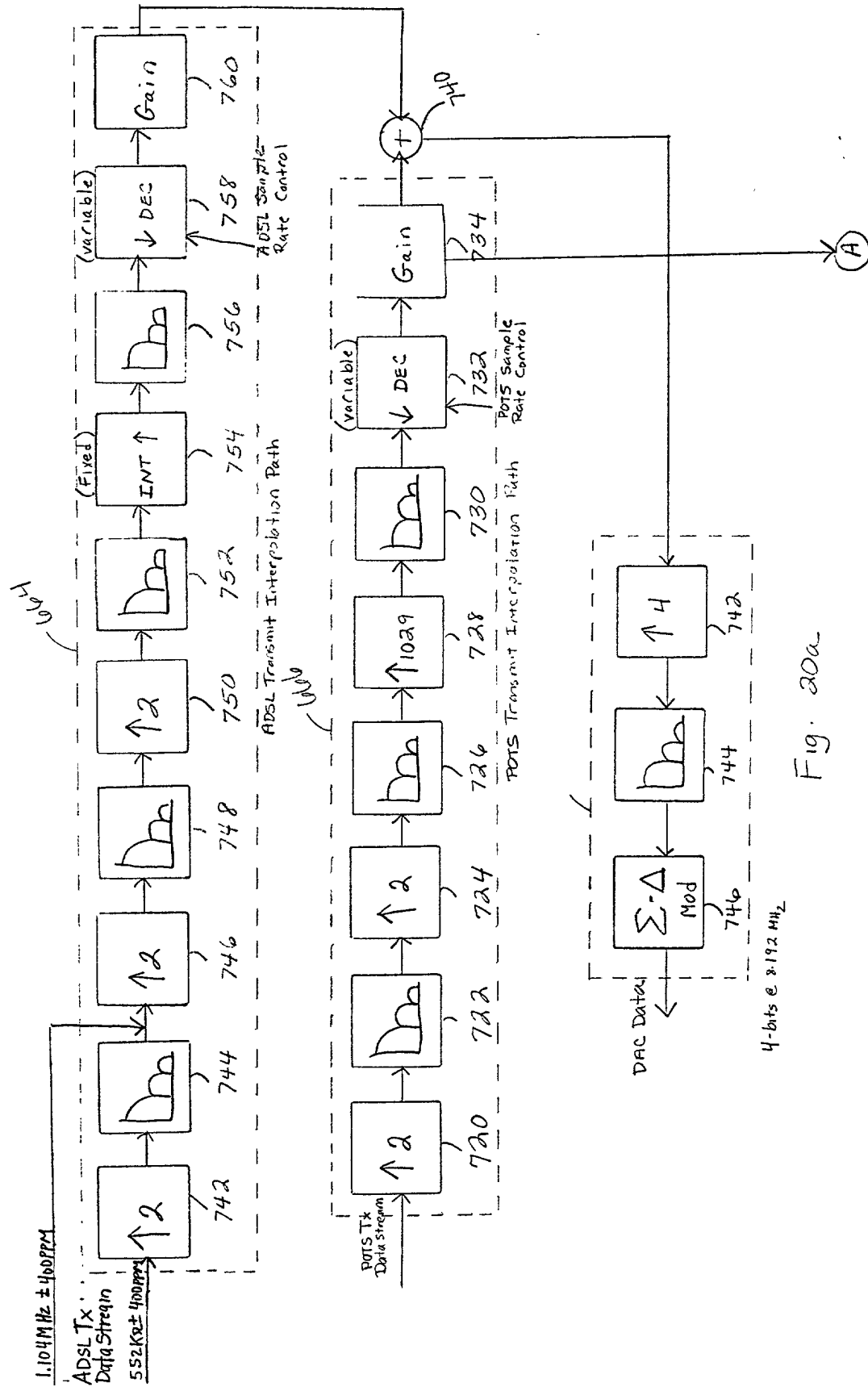


Fig. 20a

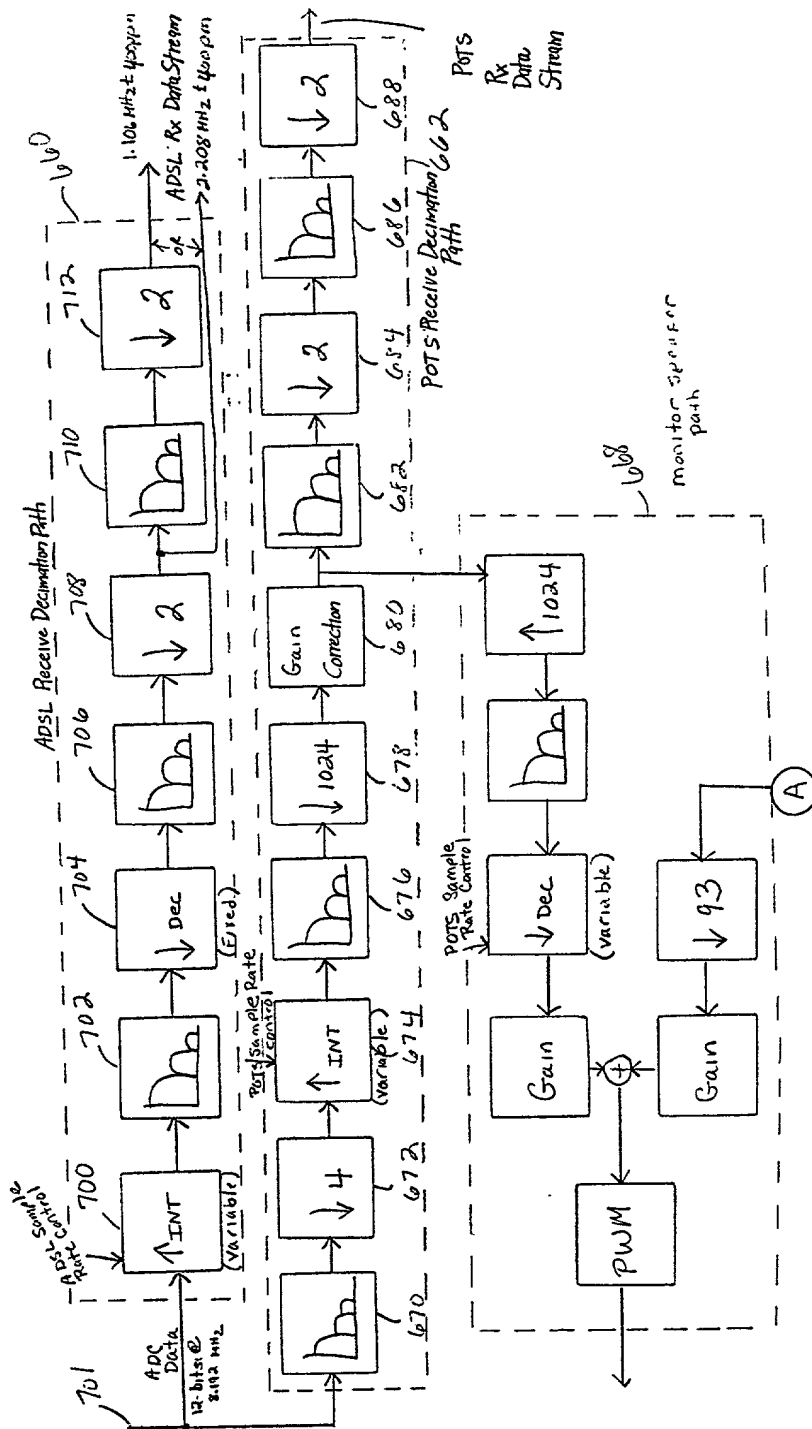
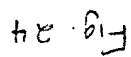
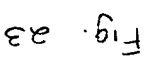
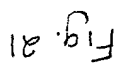
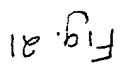


Fig 20b



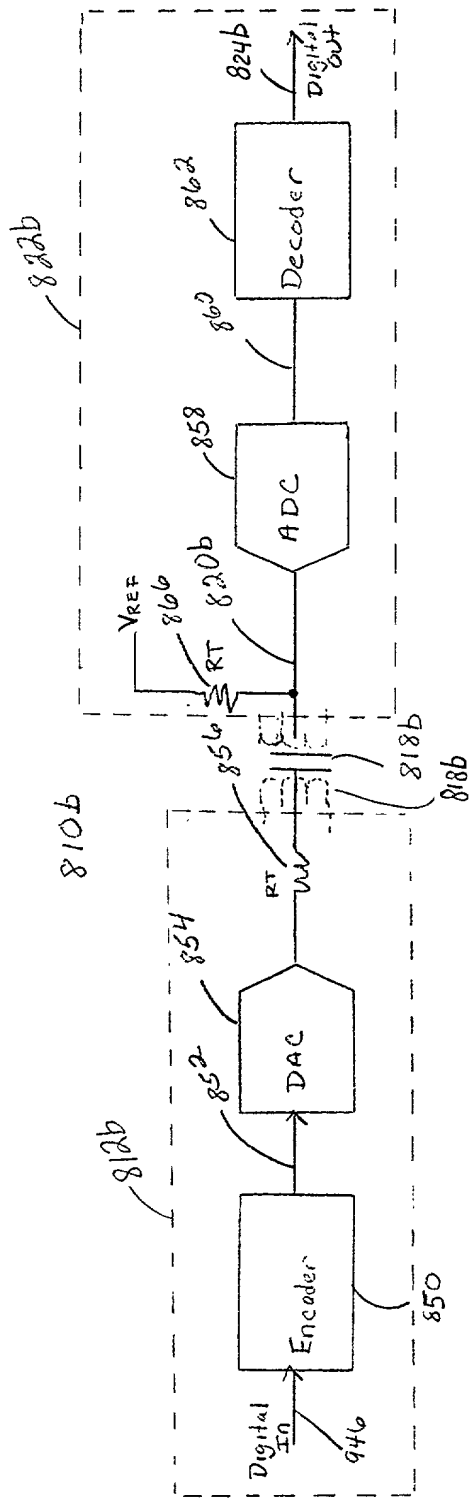


Fig. 25

ENCODER

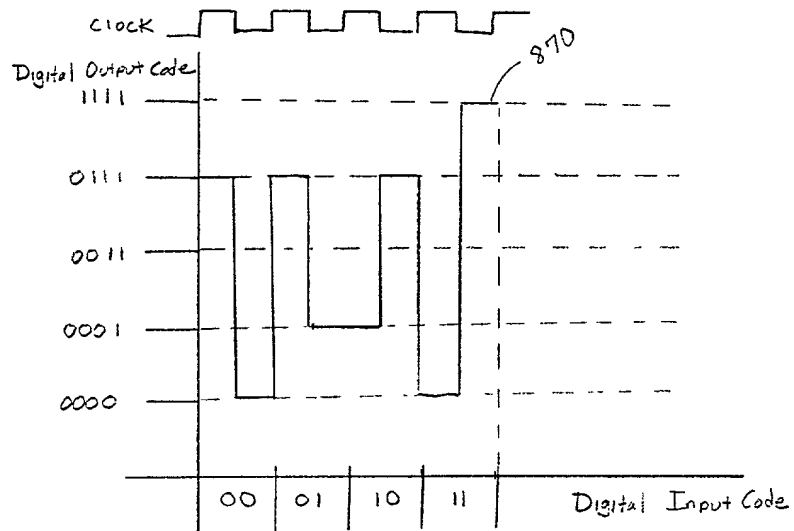


Fig 26

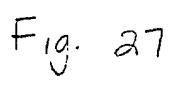
[illegible]

Fig. 27

DAC + Encoder Signal Energy

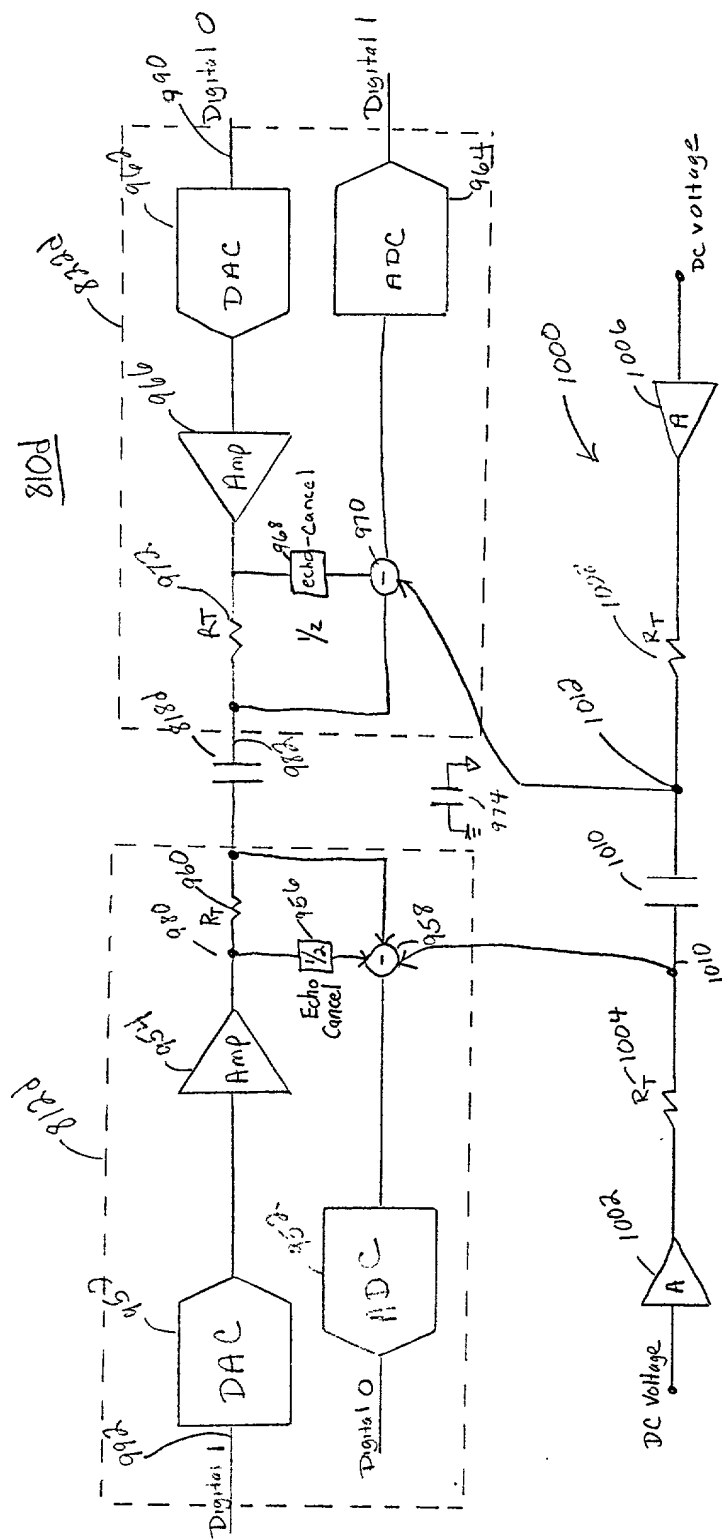
0

DAC Clock Rate

874

frequency

Fig. 28



F₉ 30

Full Barrier System

810e

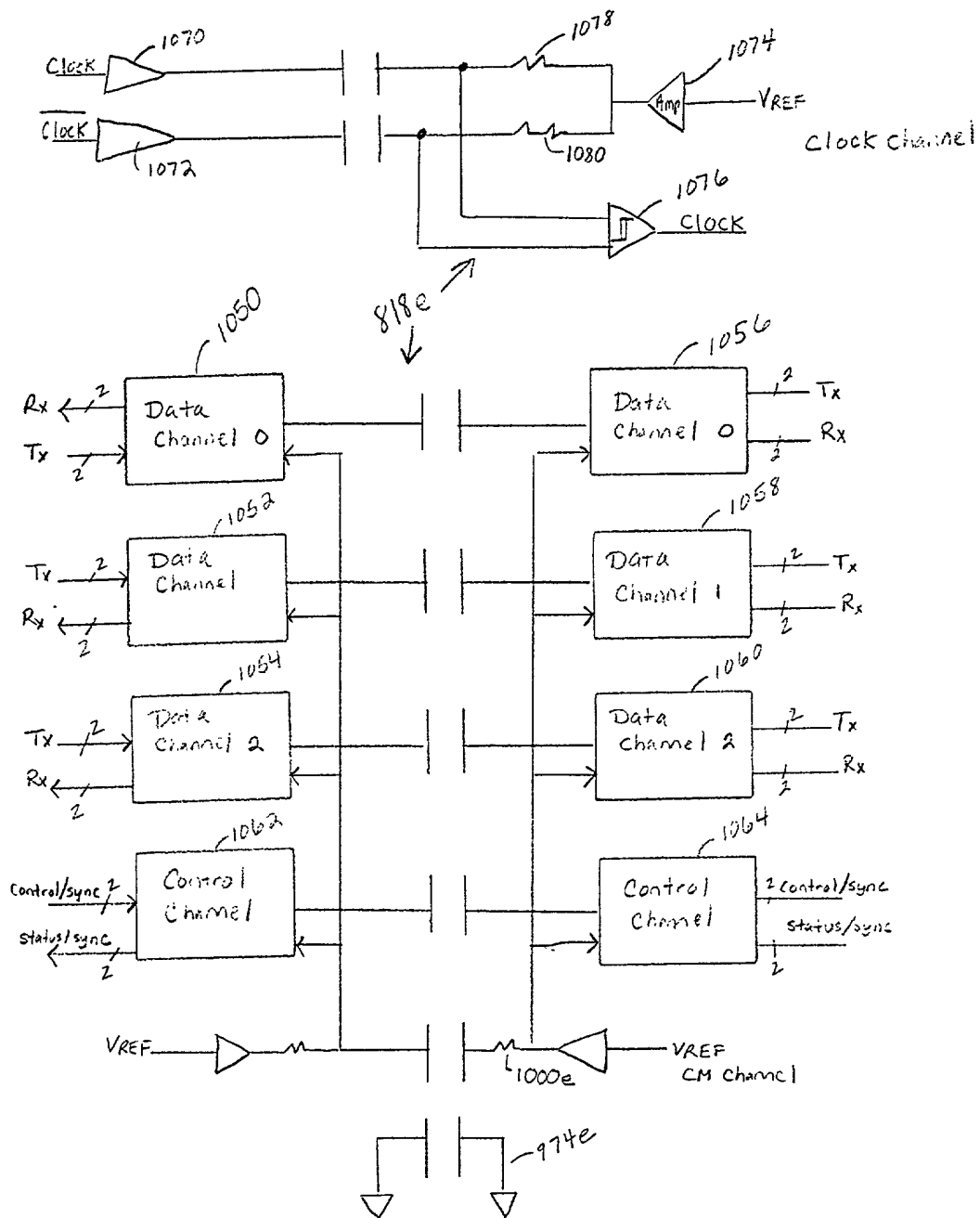


Fig. 31

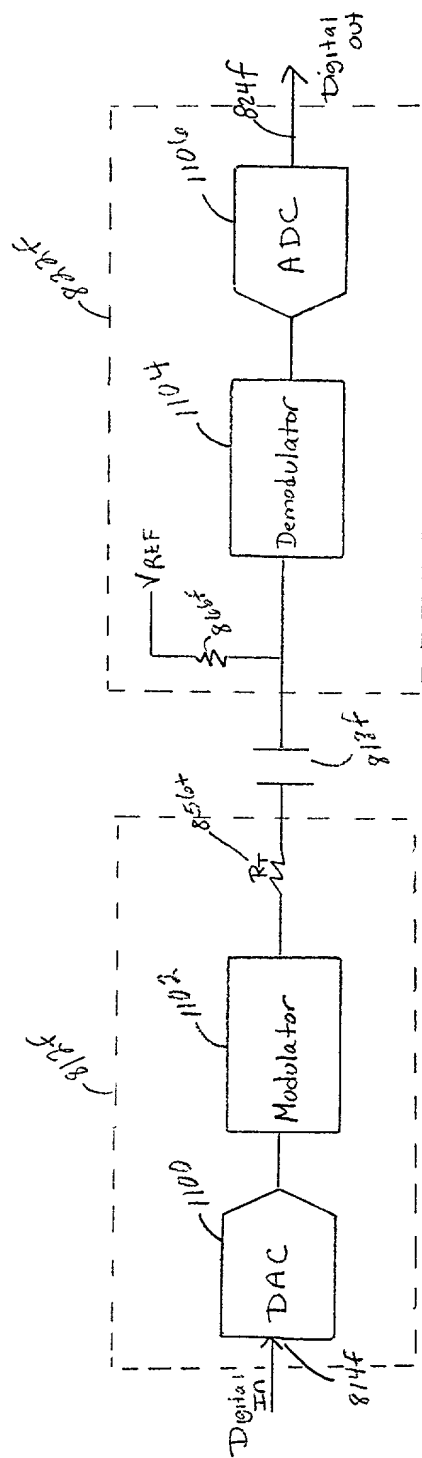


Fig. 32